

Please join experts from [Anritsu](#), [LAMSIM Enterprises](#) and [Isola](#) for a full day Signal Integrity Symposium. This event will be held from 9:15am until 4:30pm.

November 18th 2015

[Novotel Montreal Airport](#)

2599 boulevard Alfred Nobel, Ville St-Laurent, Montreal, Québec H4S 2G1, Canada.

[Montreal: click here to register](#)

November 19th 2015

[The Marshes Golf Club](#)

320 Terry Fox Dr, Kanata, ON K2K 3L1, Canada.

[Kanata: click here to register](#)

Time	Event
8:30am - 9:00am	On-site registration open
	Morning Session
9:00am - 9:15am	Welcome
9:15am - 10:00am	Critical elements for conducting accurate ultra-high speed Signal Integrity BER test
10:00am - 10:45am	Signal Integrity applications of VNA's
10:45am - 11:00am	Break
11:00am - 12:00pm	Characterizing PCB dielectrics for high speed digital and mmWave radar applications
12:00pm - 1:00pm	Lunch
	Afternoon Session
1:00pm - 1:45pm	A Practical Conductor Surface Roughness Model Using Hexagonal Close Packing of Equal Spheres
1:45pm - 2:30pm	Achieving accurate jitter tolerance test
2:30pm - 2:45pm	Break
2:45pm - 3:30pm	56G PAM4 and 56G NRZ live demo
3:30pm - 4:30pm	VNA demo, E/O and O/E

